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U.S. Patent Serial No.: 08/305,528  
Atty. Docket No.: 29284/327

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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**GROUP 2100**

APPLICANTS : WATANABE, T. et al.  
SERIAL NO. : 08/305,528  
FILED : September 13, 1994  
FOR : SEMICONDUCTOR DEVICE INCORPORATING  
INTERNAL POWER SUPPLY FOR COMPENSATING FOR  
DEVIATION ON OPERATING CONDITION AND  
FABRICATION PROCESS CONDITIONS

GROUP ART UNIT : 2511

EXAMINER : A. Zarabian

ASSISTANT COMMISSIONER FOR  
PATENTS AND TRADEMARKS  
Washington, D.C. 20231

**AMENDMENT**

SIR:

In response to the office action of October 1, 1997, the period for response having been extended herein to March 1, 1998 by payment of the requisite fee of \$400.00 for a Petition for Extension of Time Under 37 C.F.R. § 1.136(a), kindly amend the above-identified application as follows:

**IN THE CLAIMS:**

Cancel claims 27-28 without prejudice. Amend the claims as follows:

(Thrice Amended) A semiconductor memory in a chip, comprising:  
a plurality of dynamic memory cells, for storing data, each cell having at least a transfer MOS transistor;  
a plurality of word lines, each word line coupled with gate electrodes of a subset of said transfer MOS transistors of said plurality of dynamic memory cells;

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